

24. A constant voltage source circuit board as claimed in claim 22, wherein inductances between respective said first and second bypass capacitors and said load are respectively inductances of wiring lines between said first and second bypass capacitors and said load, and said first and second inductance units are wiring lines.

33. A constant voltage source circuit board as claimed in claim 19, wherein at least one of said first, second and third bypass capacitors is disposed around said load.

34. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of said wiring line is disposed around said load.

35. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of at least one of wiring lines between respective said first, second and third bypass capacitors and said load is piled over other wiring line.

37. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of at least one of wiring lines between respective said first, second and third bypass capacitors and said load is formed to pile up over at least one of said first, second and third bypass capacitors.

39. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of at least one of wiring lines between respective of said first, second and third bypass capacitors and said load is formed to pile up over said load.

REMARKS

Please reconsider the application in view of the above amendments and the following remarks. The claims have been amended solely to remove multiple dependencies. This removal is made both to comply with 37 C.F.R. § 1.75 and to reduce the additional costs of multiple dependent claims. These amendments are in no way related to issues of patentability.